

IC 74HC86 Quad 2-Input Exclusive or Gate Dip

Slug: ic-74hc86-quad-2-input-exclusive-or-gate-dip



TECHNICAL SPECIFICATIONS & SUMMARY

[Logic Gates IC] The MM74HC86 EXCLUSIVE OR gate utilizes advanced silicon-gate CMOS technology to achieve operating speeds similar to equivalent LS-TTL gates while maintaining the low power consumption and high noise immunity characteristic of standard CMOS integrated circuits. These gates are fully buffered and have a fanout of 10 LS-TTL loads. The 74HC logic family is functionally as well as pin-out compatible with the standard 74LS logic family. All inputs are protected from damage due to static discharge by internal diode clamps to VCC and ground. Features: Typical propagation delay: 9 ns Wide operating voltage range: 2-6V Low input current: 1 uA maximum Low quiescent current: 20 uA maximum (74 Series) Output drive capability: 10 LS-TTL loads Outputs Directly Interface to CMOS, NMOS, and TTL High Noise Immunity Characteristic of CMOS Devices In Compliance with JEDEC Standard No. 7A Requirements ESD Performance: HBM 2000 V; Machine Model 200 V Chip Complexity: 56 FETs or 14 Equivalent Gates These are Pb Free Devices Package

UNIT PRICE (EX. GST)

Rs. 17

WITH 18% GST

Rs. 20

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