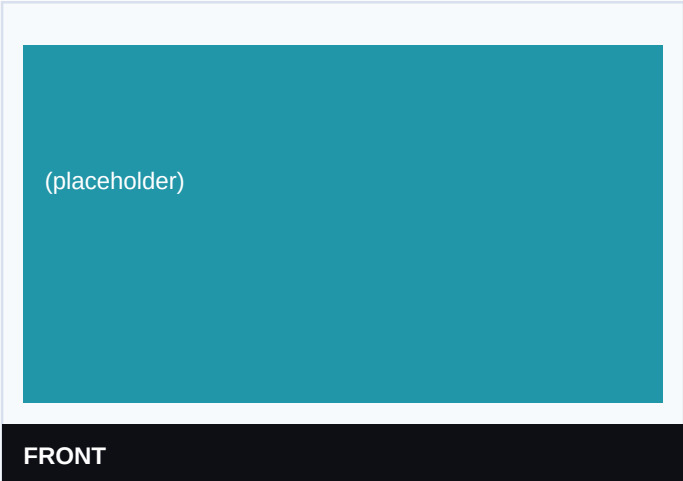
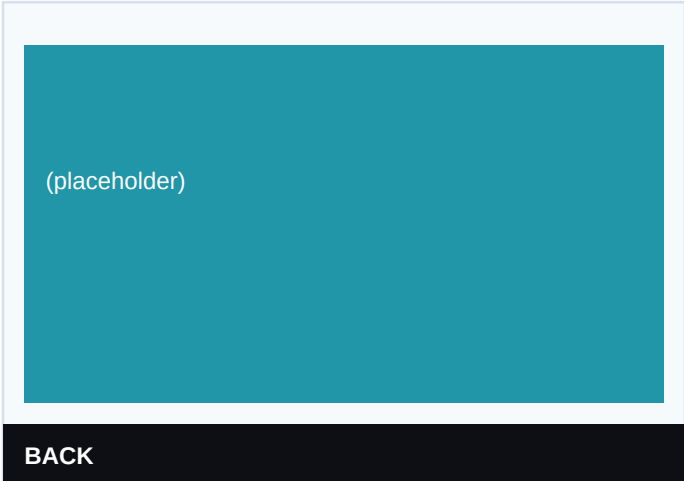
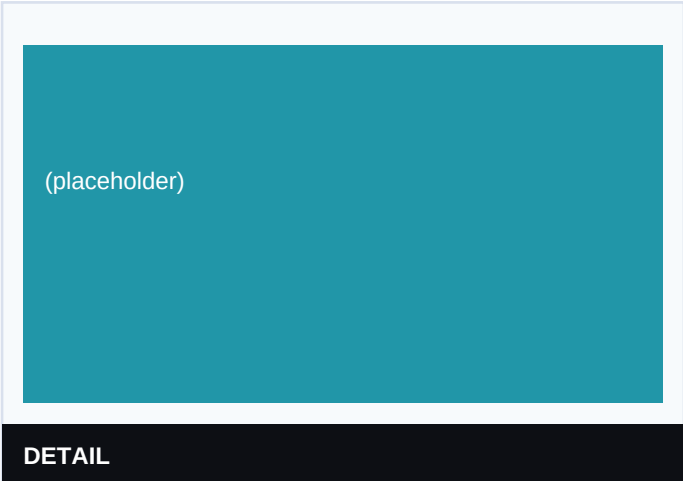
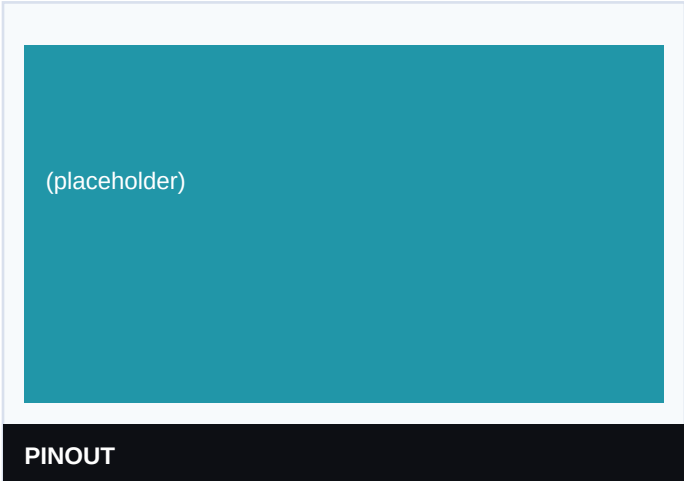


4017 Decade Counter IC

Slug: 4017-decade-counter-ic



TECHNICAL SPECIFICATIONS & SUMMARY

[Timer,Clock,Counter ICs] DESCRIPTION:- This IC 4017 is a 16-pin counter IC. It is a CMOS decade counter cum decoder circuit. It can count from zero to ten and its outputs are decoded. This saves a lot of board space and time required to build our circuits when our application demands using a counter followed by a decoder IC. IC 4017 is a digital counter plus decoder circuit. CD4017 Pin Configuration :- Pin Number Pin Name Description 1 to 7 and 9,10,11 Output pins Q0 to Q9 These are the 10 output pins on which the counting occurs, they are not in order hence verify pin diagram above 8 Vss or Ground Connected to the Ground of the circuit 12 Carry Out (CO) This pin goes high after the IC counts from 1 to 10. This is used as carry while counting. 13 Clock Enable (EN) This is an input which when made high will hold the count at the current state 14 Clock The counting happens when this clock pulse goes high , this pin is normally connected to 555 timer or other uC to produce a pulse 15 Resets As the name suggests this pin resets the count back to 1 16 Vdd / Vcc Connects to the

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